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TO: COMMISSIONER FOR PATENTS & TRADEMARKS  
UNITED STATES PATENT & TRADEMARK OFFICEFROM: Lydia McNamara  
Intellectual Property Department  
ON Semiconductor  
5005 East McDowell Road  
Phoenix, AZ 85008Tel. (602) 244-5603  
Facsimile 602-244-3169

ONS00396 P

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Date: 6/27/2006  
IN RE APPLICATION OF: Francois LHermitte  
APPLN. NO.: 10/566565  
FILED: 9/23/2003  
FOR: NEGATIVE CURRENT COMPENSATION METHOD AND CIRCUIT  
Group:  
Examiner:

CERTIFICATE OF TRANSMITTAL VIA FACSIMILE  
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I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING  
FACSIMILE TRANSMITTED TO THE PATENT AND TRADEMARK OFFICE,  
ATTN FORMALITIES OFFICER SHELBY J. VIGIL  
AT THE FAX No. 571-273-8300

By

Lydia McNamara  
Lydia McNamaraon: 6/27/06  
DateNUMBER OF PAGES INCLUDING THIS COVER PAGE: 6

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IN RE APPLICATION OF: Francois LHermite  
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FOR : NEGATIVE CURRENT COMPENSATION METHOD AND  
CIRCUIT  
ATTENTION : SHELBY J. VIGIL  
Phone : (703) 308-9140 EXT. 224  
Group :

INQUIRY REGARDING NOTIFICATION OF MISSING REQUIREMENTS

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

S I R :

We have received the 371 Formalities Notice dated 16 June 2006 requiring that we provide an oath or declaration. The Declaration forms part of the original International PCT Application at pages 4 and 5 of the PCT REQUEST; the inventor's signature appears on page 5, as shown on the attached, in accordance with standard procedure.

Please advise if this notice was sent in error.

Respectfully submitted,

Francois LHermite



ON Semiconductor  
Intellectual Property Dept.  
5005 E. McDowell Road-A700  
Phoenix, AZ 85008

Lydia McNamara, for Robert F. Hightower  
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**JUN 27 2006****UNITED STATES PATENT AND TRADEMARK OFFICE**UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
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U.S. APPLICATION NUMBER NO.	FIRST NAMED APPLICANT	ATTY. DOCKET NO.
10/566,565	Francois LHermite	ONS00396

INTERNATIONAL APPLICATION NO.
PCT/US03/30581

27255

SEMICONDUCTOR COMPONENTS INDUSTRIES, LLC  
ATTN: ROBERT D. ATKINS/LAW DEPT./MD 1700  
P.O. BOX 62890  
PHOENIX, AZ 85082-2890**RECEIVED**

I.A. FILING DATE	PRIORITY DATE
09/23/2003	

**CONFIRMATION NO. 7364****371 FORMALITIES LETTER**

\*OC000000019294583\*

**JUN 23 2006**ON SEMICONDUCTOR  
PATENT ADMINISTRATION

Date Mailed: 06/16/2006

**NOTIFICATION OF MISSING REQUIREMENTS UNDER 35 U.S.C. 371 IN THE UNITED STATES DESIGNATED/ELECTED OFFICE (DO/EO/US)**

The following items have been submitted by the applicant or the IB to the United States Patent and Trademark Office as a Designated / Elected Office (37 CFR 1.495).

- Copy of the International Application filed on 01/30/2006
- Copy of the International Search Report filed on 01/30/2006
- Information Disclosure Statements filed on 01/30/2006
- Copy of references cited in ISR filed on 01/30/2006
- U.S. Basic National Fees filed on 01/30/2006
- Assignment filed on 01/30/2006

The applicant needs to satisfy supplemental fees problems indicated below.

The following items **MUST** be furnished within the period set forth below in order to complete the requirements for acceptance under 35 U.S.C. 371:

- Oath or declaration of the inventors, in compliance with 37 CFR 1.497(a) and (b), identifying the application by the international application number and international filing date.
- To avoid abandonment, a surcharge (for late submission of filing fee, search fee, examination fee or oath or declaration) as set forth in 37 CFR 1.492(h) of \$130 for a non-small entity, must be submitted with the missing items identified in this letter.

**SUMMARY OF FEES DUE:**

Total additional fees required for this application is \$130 for a Large Entity:

- \$130 Surcharge.